



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of Dimitry Gorinevsky

Serial Number: 10/749,694

Filed: December 29, 2003

For:

IMAGE DEBLURRING WITH A SYSTOLIC ARRAY PROCESSOR

Group Art Unit:

Examiner:

Docket No. H0006745

NON FEE IDS SUBMISSION

To the Commissioner of Patents and Trademarks:

Sir:

Enclosed is PTO/SB/08B Information Disclosure Statement by Applicant and a copy of the cited articles. This submission is within three months of the PCT Search report date of January 11, 2006, and no fee is due.

DATE: Jebny (4 2006

Respectfully submitted, Dimitry Gorinevsky

By his Attorney

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CERTIFICATE OF MAILING

I hereby certify that the attached correspondence is being deposited with the United States Postal Service and First Class Mail in an envelope addressed to: Mail Stop IDS, Commissioner for Patents, PO Box 1450, Alexandria, VA, 22313-1450, on the date appearing below.

DATE:

Respectfully submitted,

John S. Munda

PTO/SB/08B (08-03) Approved for use through 07/31/2006. OMB 0651-0031

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		Application Number	10/749,694	
INFORMA	ATION DISCLOSUR	Filing Date	December 29, 2003	
STATEM	ENT BY APPLICAN	First Named Inventor	Diimitry Gorinevsky	
41		Art Unit		
(Use as many sheets as necessary)		Examiner Name		
Sheet	of	Attorney Docket Number	H0006745US	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		R. M. OWENS et al. Computer Vision on the MGAP, Proceedings, 1993, Computer Architectures for Machine Perception, 1995, pages 337-338.	
		D. Gorinevsky et al. "Optimization-based Tuning of Low-bandwidth Control in Spatially Distributed Systems, Proceedings American Control Conference, 4 June, 2003, p 1-15.	
		J. Biemond et al. Iterative Methods for Image Deblurring, Proceedings of IEEE, Vol. 78, no. 5, May 1990, (1990-1995), abstract	
		D. E. Dudgeon. An Iterative Implementation for 2-D Digital Filters, ICASSp 80 Proc., IEE International Conf. on Acoustics, Speech and Signal Processing, 1980, pages 741-744.	
		O.C. Macnally et al. A 40 Megasample IIR Filter Chip, Pro. Int. Conf. ASAP, IEEE, 1991, pages 416-430.	
		T.F. Quatieri et al. Implementation of 2-D Digital Filters by Iterative Methods, IEEE Trans. on Acoustics, Speech and Signal Processing, V. AASP-30, No. 3, 1982, p 473-487.	•
		T. F. Quatieri et al. Extensions of 2-D Iterative Digital Filters, ICASSP 81, Proceedings of the 1981 IEEE International Conf. On Acoustics etc., 1981, pp. 708-711.	
		K. S. Ray et al. Recursive Least Square Technique and Parallel Implementation Approach for Image Processing, Proc. of Tencon 87, 1987 ICCC Region 10 Conference. Abstract.	

Examiner	Date
Signature	Considered

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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